Hit List



Search Results - Record(s) 181 through 210 of 627 returned.

☐ 181. Document ID: US 5987598 A

L1: Entry 181 of 627

File: USPT

Nov 16, 1999

US-PAT-NO: 5987598

DOCUMENT-IDENTIFIER: US 5987598 A

TITLE: Method and system for tracking instruction progress within a data processing

system

DATE-ISSUED: November 16, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 712/227 US-CL-CURRENT: 712/227

FIELD-OF-SEARCH: 712/227

| Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. D | | | - | | | | | | | | | |
|---|------------|------------|-------|--------|----------------|------|-----------|-----------|-------------|--------|-----|----------|
| Diam D | Full Title | e Citation | Front | Review | Classification | Date | Reference | Seguences | Attachments | Claims | KMC | Draw, De |

☐ 182. Document ID: US 5987592 A

L1: Entry 182 of 627

File: USPT

Nov 16, 1999

US-PAT-NO: 5987592

DOCUMENT-IDENTIFIER: US 5987592 A

TITLE: Flexible resource access in a microprocessor

DATE-ISSUED: November 16, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{9/30}$

US-CL-ISSUED: 712/200; 712/208, 712/227, 712/42 US-CL-CURRENT: 712/200; 712/208, 712/227, 712/42

FIELD-OF-SEARCH: 712/200, 712/208, 712/227, 712/42



☐ 183. Document ID: US 5978908 A

L1: Entry 183 of 627

File: USPT

Nov 2, 1999

US-PAT-NO: 5978908

DOCUMENT-IDENTIFIER: US 5978908 A

** See image for Certificate of Correction **

TITLE: Computer instruction supply

DATE-ISSUED: November 2, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 712/240; 712/238, 712/239 US-CL-CURRENT: 712/240; 712/238, 712/239

FIELD-OF-SEARCH: 395/587, 395/586, 712/238, 712/239, 712/240

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw, Do

☐ 184. Document ID: US 5978902 A

L1: Entry 184 of 627

File: USPT

Nov 2, 1999

US-PAT-NO: 5978902

DOCUMENT-IDENTIFIER: US 5978902 A

** See image for Certificate of Correction **

TITLE: Debug interface including operating system access of a serial/parallel debug port

DATE-ISSUED: November 2, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 712/227; 714/27 US-CL-CURRENT: 712/227; 714/27

FIELD-OF-SEARCH: 712/227, 714/27, 714/30, 714/38, 714/28, 395/500.49

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw De

☐ 185. Document ID: US 5978838 A

L1: Entry 185 of 627 File: USPT Nov 2, 1999

US-PAT-NO: 5978838

DOCUMENT-IDENTIFIER: US 5978838 A

TITLE: Coordination and synchronization of an asymmetric, single-chip, dual

multiprocessor

Record List Display Page 3 of 13

DATE-ISSUED: November 2, 1999

INT-CL: [06] G06 F 15/16

US-CL-ISSUED: 709/208 US-CL-CURRENT: 709/208

FIELD-OF-SEARCH: 395/200.38, 209/208

Full Title Citation Front Review Classification Date Reference Citation Claims KWIC Draw, De

☐ 186. Document ID: US 5974543 A

L1: Entry 186 of 627

File: USPT

Oct 26, 1999

US-PAT-NO: 5974543

DOCUMENT-IDENTIFIER: US 5974543 A

TITLE: Apparatus and method for performing subroutine call and return operations

DATE-ISSUED: October 26, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/32

US-CL-ISSUED: 712/240 US-CL-CURRENT: 712/240

FIELD-OF-SEARCH: 712/240

Full Title Citation Front Review Classification Date Reference Review Claims KMC Draw De

☐ 187. Document ID: US 5974531 A

L1: Entry 187 of 627

File: USPT

Oct 26, 1999

US-PAT-NO: 5974531

DOCUMENT-IDENTIFIER: US 5974531 A

** See image for Certificate of Correction **

TITLE: Methods and systems of stack renaming for superscalar stack-based data

processors

DATE-ISSUED: October 26, 1999

INT-CL: [06] G06 F 12/02

US-CL-ISSUED: 712/202; 712/217 US-CL-CURRENT: 712/202; 712/217

FIELD-OF-SEARCH: 712/202, 712/217

Full Title Citation Front Review Classification Date Reference Sources Attachnesis Claims KWIC Draw De

☐ 188. Document ID: US 5968163 A

L1: Entry 188 of 627

File: USPT

Oct 19, 1999

US-PAT-NO: 5968163

DOCUMENT-IDENTIFIER: US 5968163 A

TITLE: Microcode scan unit for scanning microcode instructions using predecode data

DATE-ISSUED: October 19, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/30

US-CL-ISSUED: 712/204; 712/210, 712/212, 712/213 US-CL-CURRENT: 712/204; 712/210, 712/212, 712/213

FIELD-OF-SEARCH: 999/389, 399/389, 712/204, 712/210, 712/212, 712/213

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw. De

☐ 189. Document ID: US 5966530 A

L1: Entry 189 of 627

File: USPT

Oct 12, 1999

US-PAT-NO: 5966530

DOCUMENT-IDENTIFIER: US 5966530 A

TITLE: Structure and method for instruction boundary machine state restoration

DATE-ISSUED: October 12, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 395/591; 395/183.14 US-CL-CURRENT: 712/244; 714/38

FIELD-OF-SEARCH: 395/591, 395/183.14

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWC Draw. De

☐ 190. Document ID: US 5964870 A

L1: Entry 190 of 627

File: USPT

Oct 12, 1999

US-PAT-NO: 5964870

DOCUMENT-IDENTIFIER: US 5964870 A

** See image for <u>Certificate of Correction</u> **

TITLE: Method and apparatus for using function context to improve branch

DATE-ISSUED: October 12, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 712/240; 712/238, 712/239 US-CL-CURRENT: 712/240; 712/238, 712/239

FIELD-OF-SEARCH: 395/587, 395/586, 712/239, 712/240, 712/238

Full Title Citation Front Review Classification Date Reference ਜ਼ਿਆਪੁਰਸ਼ਟਸਤ ਪਿੰਡਰਵਿਸ਼ਟਸਤ Claims KWIC Draw. De

☐ 191. Document ID: US 5964869 A

L1: Entry 191 of 627

File: USPT

Oct 12, 1999

US-PAT-NO: 5964869

DOCUMENT-IDENTIFIER: US 5964869 A

TITLE: Instruction fetch mechanism with simultaneous prediction of control-flow

instructions

DATE-ISSUED: October 12, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/38

US-CL-ISSUED: 712/236; 712/240 US-CL-CURRENT: 712/236; 712/240

FIELD-OF-SEARCH: 395/583, 395/587, 395/585, 395/586, 712/236, 712/238, 712/239,

712/240

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. De

☐ 192. Document ID: US 5963596 A

L1: Entry 192 of 627

File: USPT

Oct 5, 1999

US-PAT-NO: 5963596

DOCUMENT-IDENTIFIER: US 5963596 A

TITLE: Audio decoder circuit and method of operation

DATE-ISSUED: October 5, 1999

INT-CL: [06] G06 F 11/00, H04 N 7/68

US-CL-ISSUED: 375/243; 375/316, 382/246 US-CL-CURRENT: 375/243; 375/316, 382/246

FIELD-OF-SEARCH: 375/242, 375/243, 375/316, 382/246

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw, De

☐ 193. Document ID: US 5963565 A

L1: Entry 193 of 627 File: USPT Oct 5, 1999

US-PAT-NO: 5963565

DOCUMENT-IDENTIFIER: US 5963565 A

TITLE: Apparatus and method for external supervision of electronic test equipment

operating parameters

DATE-ISSUED: October 5, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{19/00}$

US-CL-ISSUED: 371/22.1; 702/123 US-CL-CURRENT: 714/724; 702/123

FIELD-OF-SEARCH: 364/580, 371/22.1, 371/22.31, 371/22.4, 702/118, 702/119, 702/121,

702/123

☐ 194. Document ID: US 5961636 A

L1: Entry 194 of 627 File: USPT Oct 5, 1999

US-PAT-NO: 5961636

DOCUMENT-IDENTIFIER: US 5961636 A

TITLE: Checkpoint table for selective instruction flushing in a speculative

execution unit

DATE-ISSUED: October 5, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/302

US-CL-ISSUED: 712/228; 712/222 US-CL-CURRENT: 712/228; 712/222

FIELD-OF-SEARCH: 395/569, 712/222, 712/228

Full Title Citation Front Review Classification Date Reference Reference Reference Reference Review Claims Kill Draw. De

☐ 195. Document ID: US 5961630 A

L1: Entry 195 of 627 File: USPT Oct 5, 1999

US-PAT-NO: 5961630

DOCUMENT-IDENTIFIER: US 5961630 A

** See image for Certificate of Correction **

TITLE: Method and apparatus for handling dynamic structural hazards and exceptions by using post-ready latency

DATE-ISSUED: October 5, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 712/200; 712/214, 712/215, 712/23 US-CL-CURRENT: 712/200; 712/214, 712/215, 712/23

FIELD-OF-SEARCH: 712/214, 712/215, 712/216, 712/23

Full Title Citation Front Review Classification Date Reference <u>Sequences Attachments</u> Claims KWIC Draw, Do

☐ 196. Document ID: US 5958045 A

L1: Entry 196 of 627

File: USPT

Sep 28, 1999

US-PAT-NO: 5958045

DOCUMENT-IDENTIFIER: US 5958045 A

TITLE: Start of access instruction configured to indicate an access mode for fetching memory operands in a microprocessor

DATE-ISSUED: September 28, 1999

INT-CL: [06] G06 F 9/00

US-CL-ISSUED: 712/229; 712/205, 711/118, 711/142, 711/143 US-CL-CURRENT: 712/229; 711/118, 711/142, 711/143, 712/205

FIELD-OF-SEARCH: 711/134, 711/135, 711/140, 711/133, 711/144, 711/145, 711/118, 711/119, 711/120, 711/121, 711/123, 395/570, 395/584, 395/585, 395/586, 395/587, 395/588, 395/380, 395/381, 395/383

Full Title Citation Front Review Classification Date Reference Seguences Attachments Claims KWIC Draw. De

☐ 197. Document ID: US 5958040 A

L1: Entry 197 of 627

File: USPT

Sep 28, 1999

US-PAT-NO: 5958040

DOCUMENT-IDENTIFIER: US 5958040 A

TITLE: Adaptive stream buffers

DATE-ISSUED: September 28, 1999

INT-CL: [06] G06 F 12/00

US-CL-ISSUED: 712/207; 711/122, 711/133, 711/137, 711/159, 711/213, 711/218 US-CL-CURRENT: 712/207; 711/122, 711/133, 711/137, 711/159, 711/213, 711/218

FIELD-OF-SEARCH: 395/381, 395/382, 395/383, 711/5, 711/122, 711/137, 711/157, 711/213, 711/221, 711/118, 711/119, 711/125, 711/133, 711/140, 711/159, 711/169, 711/204, 711/218, 712/205, 712/206, 712/207

Full Title Citation Front Review Classification Date Reference

☐ 198. Document ID: US 5951679 A

L1: Entry 198 of 627

File: USPT

Sep 14, 1999

US-PAT-NO: 5951679

DOCUMENT-IDENTIFIER: US 5951679 A

** See image for Certificate of Correction **

TITLE: Microprocessor circuits, systems, and methods for issuing successive iterations of a short backward branch loop in a single cycle

DATE-ISSUED: September 14, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 712/241 US-CL-CURRENT: 712/241

FIELD-OF-SEARCH: 399/588, 712/241

Full Title Citation Front Review Classification Date Reference Management Claims KWIC Draw, De

☐ 199. Document ID: US 5951675 A

L1: Entry 199 of 627

File: USPT

Sep 14, 1999

US-PAT-NO: 5951675

DOCUMENT-IDENTIFIER: US 5951675 A

TITLE: Instruction alignment unit employing dual instruction queues for high

frequency instruction dispatch

DATE-ISSUED: September 14, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 712/215; 712/23 US-CL-CURRENT: 712/215; 712/23

FIELD-OF-SEARCH: 395/391, 395/800.23, 712/23, 712/215

Full Title Citation Front Review Classification Date Reference Superior Claims KWC Draw, De

☐ 200. Document ID: US 5948099 A

L1: Entry 200 of 627 File: USPT Sep 7, 1999

US-PAT-NO: 5948099

DOCUMENT-IDENTIFIER: US 5948099 A

TITLE: Apparatus and method for swapping the byte order of a data item to effectuate memory format conversion

DATE-ISSUED: September 7, 1999

INT-CL: [06] G06 F 9/315

US-CL-ISSUED: 712/225; 711/201, 708/209 US-CL-CURRENT: 712/225; 708/209, 711/201

FIELD-OF-SEARCH: 395/375, 395/500, 395/380, 395/566, 364/715.08, 711/201

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 201. Document ID: US 5948095 A

L1: Entry 201 of 627

File: USPT

Sep 7, 1999

US-PAT-NO: 5948095

DOCUMENT-IDENTIFIER: US 5948095 A

TITLE: Method and apparatus for prefetching data in a computer system

DATE-ISSUED: September 7, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/38

US-CL-ISSUED: 712/200; 712/207 US-CL-CURRENT: 712/200; 712/207

FIELD-OF-SEARCH: 395/705, 395/706, 712/207, 712/23, 711/122, 711/117, 711/137

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 202. Document ID: US 5943249 A

L1: Entry 202 of 627

File: USPT

Aug 24, 1999

US-PAT-NO: 5943249

DOCUMENT-IDENTIFIER: US 5943249 A

TITLE: Method and apparatus to perform pipelined denormalization of floating-point

results

DATE-ISSUED: August 24, 1999

INT-CL: [06] G06 F 7/52

US-CL-ISSUED: 364/748.02 US-CL-CURRENT: 708/496

FIELD-OF-SEARCH: 364/748.01, 364/748.02, 364/748.03, 364/748.04, 364/748.07,

364/748.09, 395/563

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 203. Document ID: US 5941985 A

L1: Entry 203 of 627

File: USPT

Aug 24, 1999

US-PAT-NO: 5941985

DOCUMENT-IDENTIFIER: US 5941985 A

TITLE: Branch instruction prediction method

DATE-ISSUED: August 24, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/38

US-CL-ISSUED: 712/240 US-CL-CURRENT: 712/240

FIELD-OF-SEARCH: 395/586, 395/587, 712/239, 712/240

Full Title Citation Front Review Classification Date Reference **Schuerces Attachments** Claims KMC Draw, De

☐ 204. Document ID: US 5941984 A

L1: Entry 204 of 627

File: USPT

Aug 24, 1999

US-PAT-NO: 5941984

DOCUMENT-IDENTIFIER: US 5941984 A

TITLE: Data processing device

DATE-ISSUED: August 24, 1999

INT-CL: [06] G06 F 15/16

US-CL-ISSUED: 712/218; 712/23, 712/24 US-CL-CURRENT: 712/218; 712/23, 712/24

FIELD-OF-SEARCH: 395/800.23, 395/800.24, 395/394, 712/218, 712/23, 712/24

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw. De

☐ 205. Document ID: US 5941982 A

L1: Entry 205 of 627

File: USPT

Aug 24, 1999

US-PAT-NO: 5941982

DOCUMENT-IDENTIFIER: US 5941982 A

TITLE: Efficient self-timed marking of lengthy variable length instructions

DATE-ISSUED: August 24, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/30

US-CL-ISSUED: 712/210 US-CL-CURRENT: 712/210

FIELD-OF-SEARCH: 712/210

Full Title Citation Front Review Classification Date Reference <u>Segundant Attackness</u> Claims KWIC Draw De

☐ 206. Document ID: US 5941980 A

L1: Entry 206 of 627

File: USPT

Aug 24, 1999

US-PAT-NO: 5941980

DOCUMENT-IDENTIFIER: US 5941980 A

TITLE: Apparatus and method for parallel decoding of variable-length instructions in a superscalar pipelined data processing system

DATE-ISSUED: August 24, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{9/30}$

US-CL-ISSUED: 712/204; 712/23, 712/206, 712/207, 712/210, 712/212, 712/213, 712/215

US-CL-CURRENT: 712/204; 712/206, 712/207, 712/210, 712/212, 712/213, 712/215,

712/23

FIELD-OF-SEARCH: 395/380, 395/381, 395/382, 395/383, 395/386, 395/388, 395/389, 711/125, 712/204, 712/205, 712/206, 712/207, 712/210, 712/212, 712/213, 712/23

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw De

☐ 207. Document ID: US 5940601 A

L1: Entry 207 of 627

File: USPT

Aug 17, 1999

US-PAT-NO: 5940601

DOCUMENT-IDENTIFIER: US 5940601 A

TITLE: Control circuit and method for a first-in first-out data pipeline

DATE-ISSUED: August 17, 1999

INT-CL: [06] G06 F 13/00

US-CL-ISSUED: 395/309; 365/189.05 US-CL-CURRENT: 710/305; 365/189.05

FIELD-OF-SEARCH: 395/309, 365/189.05, 365/220

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw. De

☐ 208. Document ID: US 5940589 A

L1: Entry 208 of 627

File: USPT

Aug 17, 1999

US-PAT-NO: 5940589

DOCUMENT-IDENTIFIER: US 5940589 A

TITLE: Method and apparatus for validating a subscriber terminal on a

telecommunication network

DATE-ISSUED: August 17, 1999

INT-CL: [06] H04 M 11/00

US-CL-ISSUED: 395/186; 379/58, 455/26.1 US-CL-CURRENT: 713/200; 455/26.1, 455/411

FIELD-OF-SEARCH: 395/186, 395/187.01, 379/58-63, 379/111-115, 455/26.1

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw De

☐ 209. Document ID: US 5938759 A

L1: Entry 209 of 627

File: USPT

Aug 17, 1999

US-PAT-NO: 5938759

DOCUMENT-IDENTIFIER: US 5938759 A

** See image for Certificate of Correction **

TITLE: Processor instruction control mechanism capable of decoding register instructions and immediate instructions with simple configuration

DATE-ISSUED: August 17, 1999

INT-CL: [06] G06 F 9/30

US-CL-ISSUED: 712/209; 712/229 US-CL-CURRENT: 712/209; 712/229

FIELD-OF-SEARCH: 712/209, 712/229

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw. De 210. Document ID: US 5935216 A

File: USPT

US-PAT-NO: 5935216

L1: Entry 210 of 627

DOCUMENT-IDENTIFIER: US 5935216 A

Aug 10, 1999

TITLE: Methods for operating parallel computing systems employing sequenced communications

DATE-ISSUED: August 10, 1999

INT-CL: [06] G06 F 15/163

US-CL-ISSUED: 709/248; 712/17 US-CL-CURRENT: 709/248; 712/17

FIELD-OF-SEARCH: 364/2MSFile, 364/9MSFile, 395/200, 395/800, 395/800.16,

395/800.17, 395/200.31, 395/200.78

| Full Title Citation Front Review Classification Date Reference Sections Atte | Chinests Claims KMC Draw [|
|--|-----------------------------------|
| Generate Collection Print Fwd Refs Selwa Ref | |
| Generate Collection Print Fwd Refs Bkwd Ref | |
| Term | Documents |
| TREAT | 121959 |
| TREAT | 121959 |
| ((TREAT.XA.) OR (TREAT.XP.)).PGPB,USPT. | 627 |
| (TREAT.XA. OR TREAT.XP.).PGPB,USPT. | 627 |

Display Format: CLS Change Format

Previous Page Next Page Go to Doc#

Hit List



Search Results - Record(s) 211 through 240 of 627 returned.

☐ 211. Document ID: US 5933629 A

L1: Entry 211 of 627

File: USPT

Aug 3, 1999

US-PAT-NO: 5933629

DOCUMENT-IDENTIFIER: US 5933629 A

TITLE: Apparatus and method for detecting microbranches early

DATE-ISSUED: August 3, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/598; 395/572 US-CL-CURRENT: 712/248; 712/231

FIELD-OF-SEARCH: 395/381, 395/382, 395/383, 395/384, 395/386, 395/387, 395/389,

395/598, 395/572, 711/123, 711/125

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 212. Document ID: US 5933626 A

L1: Entry 212 of 627

File: USPT

Aug 3, 1999

US-PAT-NO: 5933626

DOCUMENT-IDENTIFIER: US 5933626 A

TITLE: Apparatus and method for tracing microprocessor instructions

DATE-ISSUED: August 3, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 395/568; 395/183.14 US-CL-CURRENT: 712/227; 714/38

FIELD-OF-SEARCH: 395/568, 395/183.14

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw De

☐ 213. Document ID: US 5931944 A

L1: Entry 213 of 627

File: USPT

Aug 3, 1999

US-PAT-NO: 5931944

DOCUMENT-IDENTIFIER: US 5931944 A

TITLE: Branch instruction handling in a self-timed marking system

DATE-ISSUED: August 3, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/30

US-CL-ISSUED: 712/239; 712/204, 712/210, 712/212, 712/213, 712/206 US-CL-CURRENT: 712/239; 712/204, 712/206, 712/210, 712/212, 712/213

FIELD-OF-SEARCH: 712/204, 712/206, 712/210, 712/212, 712/213, 712/239

Full Title Citation Front Review Classification Date Reference **Sequences Attended Sequences** Claims KWIC Draw, Do

☐ 214. Document ID: US 5926832 A

L1: Entry 214 of 627

File: USPT

Jul 20, 1999

US-PAT-NO: 5926832

DOCUMENT-IDENTIFIER: US 5926832 A

TITLE: Method and apparatus for aliasing memory data in an advanced microprocessor

DATE-ISSUED: July 20, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{12/00}$

US-CL-ISSUED: 711/141; 395/500 US-CL-CURRENT: 711/141; 703/23

FIELD-OF-SEARCH: 711/141, 711/142, 711/143, 711/144, 711/145, 711/146, 395/468,

395/469, 395/470, 395/471, 395/472, 395/473, 395/500

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw. De

☐ 215. Document ID: US 5923892 A

L1: Entry 215 of 627

File: USPT

Jul 13, 1999

US-PAT-NO: 5923892

DOCUMENT-IDENTIFIER: US 5923892 A

** See image for <u>Certificate of Correction</u> **

TITLE: Host processor and coprocessor arrangement for processing platform-

independent code

DATE-ISSUED: July 13, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{15}/\underline{17}$

US-CL-ISSUED: 395/800.31; 395/800.34, 395/800.36, 395/200.38

US-CL-CURRENT: 712/31; 709/208, 712/34, 712/36

FIELD-OF-SEARCH: 395/200.38, 395/200.39, 395/200.4, 395/200.41, 395/200.76,

395/800.31, 395/800.34, 395/800.36

Full Title Citation Front Review Classification Date Reference State (1995) Machineris Claims KMC Draw. De

☐ 216. Document ID: US 5918045 A

L1: Entry 216 of 627

File: USPT

Jun 29, 1999

US-PAT-NO: 5918045

DOCUMENT-IDENTIFIER: US 5918045 A

TITLE: Data processor and data processing system

DATE-ISSUED: June 29, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/38

US-CL-ISSUED: 395/584; 711/213 US-CL-CURRENT: 712/237; 711/213

FIELD-OF-SEARCH: 395/381, 395/383, 395/584, 711/213

Full Title Citation Front Review Classification Date Reference

☐ 217. Document ID: US 5918012 A

L1: Entry 217 of 627

File: USPT

Jun 29, 1999

US-PAT-NO: 5918012

DOCUMENT-IDENTIFIER: US 5918012 A

TITLE: Hyperlinking time-based data files

DATE-ISSUED: June 29, 1999

INT-CL: [06] $\underline{H04}$ \underline{L} $\underline{12/28}$

US-CL-ISSUED: 395/200.47; 395/200.33, 395/200.48, 345/335 US-CL-CURRENT: 709/217; 345/744, 345/760, 709/203, 709/218

FIELD-OF-SEARCH: 395/200.48, 395/200.33, 395/200.47, 395/200.34, 395/200.56,

395/200.61, 395/188, 395/146, 395/761, 345/121, 345/335, 707/501

Full Title Citation Front Review Classification Date Reference Section Affacturerits Claims KMC Draw. Do

☐ 218. Document ID: US 5915117 A

L1: Entry 218 of 627

File: USPT

Jun 22, 1999

US-PAT-NO: 5915117

DOCUMENT-IDENTIFIER: US 5915117 A

TITLE: Computer architecture for the deferral of exceptions on speculative

instructions

DATE-ISSUED: June 22, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/38

US-CL-ISSUED: 395/735; 395/591

US-CL-CURRENT: 710/262

FIELD-OF-SEARCH: 395/735, 395/591

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. De

☐ 219. Document ID: US 5915085 A

L1: Entry 219 of 627

File: USPT

Jun 22, 1999

US-PAT-NO: 5915085

DOCUMENT-IDENTIFIER: US 5915085 A

TITLE: Multiple resource or security contexts in a multithreaded application

DATE-ISSUED: June 22, 1999

INT-CL: [06] <u>H04</u> <u>L</u> <u>9/00</u>

US-CL-ISSUED: 395/186 US-CL-CURRENT: 713/200

FIELD-OF-SEARCH: 380/4, 380/25, 395/186, 395/187.01, 395/188.01

Full Title Citation Front Review Classification Date Reference Seguences Attachments Claims KMC Draw. De

☐ 220. Document ID: US 5914713 A

L1: Entry 220 of 627

File: USPT

Jun 22, 1999

US-PAT-NO: 5914713

DOCUMENT-IDENTIFIER: US 5914713 A

TITLE: Accessing data fields from a non-terminal client

DATE-ISSUED: June 22, 1999

INT-CL: [06] G06 F 13/00

US-CL-ISSUED: 345/335; 345/334, 395/200.33 US-CL-CURRENT: 345/744; 345/764, 709/203

FIELD-OF-SEARCH: 395/200.33, 395/200.5, 395/200.72, 395/326, 395/333, 395/335,

707/4, 364/282.1, 364/283.3, 345/334

Full Title Citation Front Review Classification Date Reference <u>Legislances Effectioned</u> Claims KMC Draw De

☐ 221. Document ID: US 5913049 A

L1: Entry 221 of 627

File: USPT

Jun 15, 1999

US-PAT-NO: 5913049

DOCUMENT-IDENTIFIER: US 5913049 A

TITLE: Multi-stream complex instruction set microprocessor

DATE-ISSUED: June 15, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/391; 395/376, 395/382, 395/388, 395/391, 395/392, 395/800.23,

395/800.24

US-CL-CURRENT: <u>712/215</u>

FIELD-OF-SEARCH: 395/800.23, 395/800.24, 395/391, 395/388, 395/382, 395/383,

395/376, 395/392

Full Title Citation Front Review Classification Date Reference Sequences Affachments Claims KWC Draw. De

☐ 222. Document ID: US 5913048 A

L1: Entry 222 of 627

File: USPT

Jun 15, 1999

US-PAT-NO: 5913048

DOCUMENT-IDENTIFIER: US 5913048 A

TITLE: Dispatching instructions in a processor supporting out-of-order execution

DATE-ISSUED: June 15, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/391; 395/591

US-CL-CURRENT: 712/215

FIELD-OF-SEARCH: 395/393, 395/394, 395/390, 395/391, 395/569, 395/591, 395/733, 395/734, 395/735, 395/736, 395/737, 395/738, 395/739, 395/740, 395/741, 395/742

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw De

☐ 223. Document ID: US 5912677 A

L1: Entry 223 of 627

File: USPT

Jun 15, 1999

US-PAT-NO: 5912677

DOCUMENT-IDENTIFIER: US 5912677 A

TITLE: Method for forming a sum in a signal processing system

DATE-ISSUED: June 15, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{9/305}$

US-CL-ISSUED: 345/524 US-CL-CURRENT: 345/561

FIELD-OF-SEARCH: 345/524, 395/564, 395/565

Full Title Citation Front Review Classification Date Reference Settlectes Attachments Claims KMC Draw. De

☐ 224. Document ID: US 5909587 A

L1: Entry 224 of 627

File: USPT

Jun 1, 1999

US-PAT-NO: 5909587

DOCUMENT-IDENTIFIER: US 5909587 A

TITLE: Multi-chip superscalar microprocessor module

DATE-ISSUED: June 1, 1999

INT-CL: [06] G06 F 15/00

US-CL-ISSUED: 395/800.01; 395/800.23

US-CL-CURRENT: 712/1

FIELD-OF-SEARCH: 395/800.01, 395/800.02, 395/800.1, 395/800.23, 395/800.24,

395/800.25, 395/800.32, 395/800.35, 395/800.38

Full Title Citation Front Review Classification Date Reference Seguences Attechments Claims KWIC Draw. De

☐ 225. Document ID: US 5903919 A

L1: Entry 225 of 627

File: USPT

May 11, 1999

US-PAT-NO: 5903919

DOCUMENT-IDENTIFIER: US 5903919 A

TITLE: Method and apparatus for selecting a register bank

DATE-ISSUED: May 11, 1999

INT-CL: [06] G06 F 12/06

US-CL-ISSUED: 711/220 US-CL-CURRENT: 711/220

FIELD-OF-SEARCH: 711/220, 711/221

Full Title Citation Front Review Classification Date Reference Securities Stationary Claims KMC Draw, De

☐ 226. Document ID: US 5901283 A

L1: Entry 226 of 627

File: USPT

May 4, 1999

US-PAT-NO: 5901283

DOCUMENT-IDENTIFIER: US 5901283 A

TITLE: Microcomputer

DATE-ISSUED: May 4, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 395/183.14; 395/183.11, 395/183.13, 395/568, 395/704

US-CL-CURRENT: <u>717</u>/<u>128</u>

FIELD-OF-SEARCH: 395/183.08, 395/183.11, 395/183.13, 395/183.14, 395/568, 395/704

Full Title Citation Front Review Classification Date Reference **Sequences Glackments** Claims KWC Draw. De

☐ 227. Document ID: US 5898852 A

L1: Entry 227 of 627

File: USPT

Apr 27, 1999

US-PAT-NO: 5898852

DOCUMENT-IDENTIFIER: US 5898852 A

TITLE: Load instruction steering in a dual data cache microarchitecture

DATE-ISSUED: April 27, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{9/06}$

US-CL-ISSUED: 395/390; 395/391

US-CL-CURRENT: 712/214

FIELD-OF-SEARCH: 395/390, 395/391, 711/119, 711/123, 711/137, 711/140

Full Title Citation Front Review Classification Date Reference **Sequences Attachinents** Claims KWIC Draw. Do

☐ 228. Document ID: US 5898850 A

L1: Entry 228 of 627

File: USPT

Apr 27, 1999

US-PAT-NO: 5898850

DOCUMENT-IDENTIFIER: US 5898850 A

** See image for Certificate of Correction **

TITLE: Method and system for executing a non-native mode-sensitive instruction

within a computer system

DATE-ISSUED: April 27, 1999

INT-CL: [06] G06 F 9/30

US-CL-ISSUED: 395/570; 395/384

US-CL-CURRENT: 712/229

FIELD-OF-SEARCH: 395/570, 395/384, 395/589, 395/591

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw De

☐ 229. Document ID: US 5897654 A

L1: Entry 229 of 627

File: USPT

Apr 27, 1999

US-PAT-NO: 5897654

DOCUMENT-IDENTIFIER: US 5897654 A

TITLE: Method and system for efficiently fetching from cache during a cache fill

operation

DATE-ISSUED: April 27, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{13}/\underline{00}$

US-CL-ISSUED: 711/131; 711/149, 711/133, 711/168 US-CL-CURRENT: 711/131; 711/133, 711/149, 711/168

FIELD-OF-SEARCH: 395/458, 395/460, 395/477, 395/478, 395/481, 395/495, 395/853,

711/149, 711/131, 711/133, 711/150, 711/151, 711/168

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw, De

☐ 230. Document ID: US 5896527 A

L1: Entry 230 of 627

File: USPT

Apr 20, 1999

US-PAT-NO: 5896527

DOCUMENT-IDENTIFIER: US 5896527 A

TITLE: Accessing data during the transition between program releases

DATE-ISSUED: April 20, 1999

INT-CL: [06] G06 F $\frac{17}{30}$

US-CL-ISSUED: 395/712; 707/203 US-CL-CURRENT: 717/174; 707/203

FIELD-OF-SEARCH: 345/331, 345/971, 395/671, 395/676, 395/703, 395/712, 707/201,

707/203

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw De

☐ 231. Document ID: US 5896526 A

L1: Entry 231 of 627

File: USPT

Apr 20, 1999

US-PAT-NO: 5896526

DOCUMENT-IDENTIFIER: US 5896526 A

TITLE: Programmable instruction trap system and method

DATE-ISSUED: April 20, 1999

INT-CL: [06] G06 F 11/00

US-CL-ISSUED: 395/568; 395/591, 395/183.11, 395/183.14

US-CL-CURRENT: <u>712/227</u>

FIELD-OF-SEARCH: 395/183.1, 395/183.11, 395/183.14, 395/568, 395/591, 395/739

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw. De

☐ 232. Document ID: US 5892936 A

L1: Entry 232 of 627

File: USPT

Apr 6, 1999

US-PAT-NO: 5892936

DOCUMENT-IDENTIFIER: US 5892936 A

TITLE: Speculative register file for storing speculative register states and

removing dependencies between instructions utilizing the register

DATE-ISSUED: April 6, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/392; 395/388, 395/389, 395/800.23 US-CL-CURRENT: 712/216; 712/212, 712/213, 712/23

FIELD-OF-SEARCH: 395/380, 395/388, 395/389, 395/392, 395/586, 395/800.23

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMIC Draw, De

☐ 233. Document ID: US 5892432 A

L1: Entry 233 of 627

File: USPT

Apr 6, 1999

US-PAT-NO: 5892432

DOCUMENT-IDENTIFIER: US 5892432 A

TITLE: Personal computer control and activation device utilizing a paging message

DATE-ISSUED: April 6, 1999

INT-CL: [06] G08 B 23/00

US-CL-ISSUED: 340/313; 340/825.44, 340/311.1, 379/93.25

US-CL-CURRENT: 340/7.1; 340/7.22, 379/93.25

FIELD-OF-SEARCH: 340/825.44, 340/311.1, 340/313, 379/93.25

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KWIC Draw, De

☐ 234. Document ID: US 5887188 A

L1: Entry 234 of 627

File: USPT

Mar 23, 1999

US-PAT-NO: 5887188

DOCUMENT-IDENTIFIER: US 5887188 A

TITLE: Multiprocessor system providing enhanced efficiency of accessing page mode memory by master processor

DATE-ISSUED: March 23, 1999

INT-CL: [06] G06 F 15/167

US-CL-ISSUED: 395/800.31; 711/147 US-CL-CURRENT: 712/31; 711/147

FIELD-OF-SEARCH: 395/800, 395/425, 395/200, 395/800.31, 395/200.38, 395/200.39,

395/200.4, 395/200.41, 711/147, 711/146

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw. De

☐ 235. Document ID: US 5887161 A

L1: Entry 235 of 627

File: USPT

Mar 23, 1999

US-PAT-NO: 5887161

DOCUMENT-IDENTIFIER: US 5887161 A

TITLE: Issuing instructions in a processor supporting out-of-order execution

DATE-ISSUED: March 23, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} 9/46

US-CL-ISSUED: 395/591; 395/569, 395/800.23 US-CL-CURRENT: 712/244; 712/228, 712/23 FIELD-OF-SEARCH: 395/800.23, 395/390, 395/391, 395/392, 395/393, 395/569, 395/591

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw De

☐ 236. Document ID: US 5884062 A

L1: Entry 236 of 627

File: USPT

Mar 16, 1999

US-PAT-NO: 5884062

DOCUMENT-IDENTIFIER: US 5884062 A

** See image for Certificate of Correction **

TITLE: Microprocessor with pipeline status integrity logic for handling multiple

stage writeback exceptions

DATE-ISSUED: March 16, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/394; 395/591 US-CL-CURRENT: 712/218; 712/244

FIELD-OF-SEARCH: 395/394, 395/563, 395/591

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KMC Draw. De

☐ 237. Document ID: US 5884060 A

L1: Entry 237 of 627

File: USPT

Mar 16, 1999

US-PAT-NO: 5884060

DOCUMENT-IDENTIFIER: US 5884060 A

TITLE: Processor which performs dynamic instruction scheduling at time of execution

within a single clock cycle

DATE-ISSUED: March 16, 1999

INT-CL: [06] $\underline{G06}$ \underline{F} $\underline{9/38}$, $\underline{G06}$ \underline{F} $\underline{9/28}$

US-CL-ISSUED: 395/391; 395/392

US-CL-CURRENT: 712/215

FIELD-OF-SEARCH: 395/385, 395/390, 395/391, 395/392, 395/800.23, 395/393, 364/228,

364/231.8, 364/243.4, 364/262.4, 364/DIG.1

Full Title Citation Front Review Classification Date Reference Sequences Attachments Claims KMC Draw. De

☐ 238. Document ID: US 5881274 A

L1: Entry 238 of 627

File: USPT

Mar 9, 1999

US-PAT-NO: 5881274

DOCUMENT-IDENTIFIER: US 5881274 A

** See image for Certificate of Correction **

TITLE: Method and apparatus for performing add and rotate as a single instruction within a processor

DATE-ISSUED: March 9, 1999

INT-CL: [06] G06 F 9/302, G06 F 9/305, G06 F 9/315

US-CL-ISSUED: 395/562; 364/787.01, 395/564 US-CL-CURRENT: 712/221; 708/710, 712/223

FIELD-OF-SEARCH: 395/562, 395/564, 395/565, 364/787.01, 364/787.02, 364/787.03,

364/878.04

Full Title Citation Front Review Classification Date Reference **Sequences Attachments** Claims KWIC Draw, Do

☐ 239. Document ID: US 5878272 A

L1: Entry 239 of 627

File: USPT

Mar 2, 1999

US-PAT-NO: 5878272

DOCUMENT-IDENTIFIER: US 5878272 A

TITLE: Computer system having two DMA circuits assigned to the same address space

DATE-ISSUED: March 2, 1999

INT-CL: [06] G06 F 13/42

US-CL-ISSUED: 395/823; 395/822 US-CL-CURRENT: 710/3; 710/2

FIELD-OF-SEARCH: 395/821, 395/822, 395/823, 395/828, 395/842, 395/847, 395/281,

395/306, 395/308

Full Title Citation Front Review Classification Date Reference Seguences Altachments Claims KWIC Draw. De

☐ 240. Document ID: US 5878254 A

L1: Entry 240 of 627

File: USPT

Mar 2, 1999

US-PAT-NO: 5878254

DOCUMENT-IDENTIFIER: US 5878254 A

TITLE: Instruction branching method and a processor

DATE-ISSUED: March 2, 1999

INT-CL: [06] G06 F 9/38

US-CL-ISSUED: 395/585; 395/706, 395/705, 395/580

US-CL-CURRENT: <u>712/238</u>; <u>717/141</u>, <u>717/149</u>

FIELD-OF-SEARCH: 395/580, 395/585, 395/586, 395/587, 395/705, 395/706

| Full | itle Citation Front Review Classification Date Reference Section Ale | actonesis Claims KMC Draw D |
|--------------|--|-----------------------------|
| Glear | Generate Collection Print Fwd Reis Bkwd Rei | is Generate OACS |
| | Term | Documents |
| | TREAT | 121959 |
| | TREAT | 121959 |
| | ((TREAT.XA.) OR (TREAT.XP.)).PGPB,USPT. | 627 |
| | (TREAT.XA. OR TREAT.XP.).PGPB,USPT. | 627 |

Display Format: CLS Change Format

Previous Page Next Page Go to Doc#